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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		09989762
	Filing Date		2001-11-19
	First Named Inventor	Kenneth Y. Ogami	
	Art Unit	2123	
	Examiner Name	Osborne, Luke R.	
	Attorney Docket Number	CD01175M	

### U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6825689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
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	7	6823282	B1	2004-11-23	Snyder	Entire Document
	8	7127630	B1	2006-10-24	Snyder	Entire Document

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9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
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20	6981090	B1	2005-12-27	Kutz et al.	Entire Document
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**U.S. PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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1	20050240917	A1	2005-10-27	Wu, Yuh-Cherng	Entire Document
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>
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22	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 01/22/07; 15 pages	<input type="checkbox"/>
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24	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 03/02/06; 14 pages	<input type="checkbox"/>
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28	USPTO Final Rejection for 10/002,217 (CD01207) dated 02/06/08; 10 pages	<input type="checkbox"/>
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31	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance;" Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
32	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Mar; 28 pages	<input type="checkbox"/>
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35	Julio Faura et al.; "A NOVEL MIXED SIGNAL PROGRAMMABLE DEVICE WITH ON-CHIP MICROPROCESSOR", 1997, IEEE 1997 Custom Integrated Circuits Conference; 4 pages	<input type="checkbox"/>
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37	ROBINSON, GORDON D; "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes; 6 pages	<input type="checkbox"/>
38	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	<input type="checkbox"/>



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39	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http://www.archive.org/web/20010219005250/http://cypressmicro.com-/t...>, Feb. 19, 2001; 21 pages	<input type="checkbox"/>
40	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Network Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000; 1 page	<input type="checkbox"/>
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42	Application Number 10/803,030 (CD00169C): "Programmable Microcontrollable Architecture (Mixed Analog/Digital);" Snyder; 13 pages	<input type="checkbox"/>
43	Application Number 10/033,027 (CD00232M): "Microcontrollable Programmable System on a Chip;" Snyder; 117 pages	<input type="checkbox"/>

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